

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1776	power with "clock speed"	US-PGPUB; USPAT; USOCR	OR	ON	2006/11/15 11:48
L2	52	L1.ab.	US-PGPUB; USPAT; USOCR	OR	ON	2006/11/15 11:48
L3	52	L2	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/11/15 11:48
L4	11	("5450556" "5485629" "5590368" "5625835" "5649135" "5787303" "5826054" "5978822" "5983336" "6026478" "6026486").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/11/15 11:48
L5	11	L4	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/11/15 11:49
L6	890	variable near3 latency	US-PGPUB; USPAT	OR	ON	2006/11/15 11:49
L7	137	L6 with (instruction or execution or operation)	US-PGPUB; USPAT	OR	ON	2006/11/15 11:49
L8	28	L7 and VLIW	US-PGPUB; USPAT	OR	ON	2006/11/15 11:49
L9	28	L8	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/11/15 11:49
S1	1	("6216223").PN.	USPAT; USOCR	OR	OFF	2006/05/01 15:44
S2	253218	(vary\$3 or chang\$3 or switch\$3) near2 (frequency or speed)	US-PGPUB; USPAT	OR	ON	2006/05/02 10:09
S3	10852	S2 with (pipeline or clock)	US-PGPUB; USPAT	OR	ON	2006/05/02 08:45
S4	644	S3.ab.	US-PGPUB; USPAT	OR	ON	2006/05/02 08:46
S5	9629	(class or type) near instruction	US-PGPUB; USPAT	OR	ON	2006/05/02 08:46
S6	6	S5 same S3	US-PGPUB; USPAT	OR	ON	2006/05/02 09:32
S7	0	S4 and VLIW.ab.	US-PGPUB; USPAT	OR	ON	2006/05/02 09:32

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S8	6	S4 and VLIW	US-PGPUB; USPAT	OR	ON	2006/05/02 09:32
S9	0	(variable) near2 "execution latency"	US-PGPUB; USPAT	OR	ON	2006/05/02 10:09
S10	1788769	I	US-PGPUB; USPAT	OR	ON	2006/05/02 10:09
S11	10	(variable) near2 "execution latency"	US-PGPUB; USPAT	OR	ON	2006/05/02 10:09
S12	49	("3623017").URPN.	USPAT	OR	ON	2006/05/02 13:17
S16	247	(VLIW or "very long instruction word").ab.	US-PGPUB; USPAT	OR	ON	2006/05/02 13:19
S17	12795	register near2 hold	US-PGPUB; USPAT	OR	ON	2006/05/02 13:20
S18	59	S17 and S16	US-PGPUB; USPAT	OR	ON	2006/05/02 13:22
S19	8	"execution latency".ab.	US-PGPUB; USPAT	OR	ON	2006/05/02 13:24
S20	13	S16 and latency.ab.	US-PGPUB; USPAT	OR	ON	2006/05/02 13:25
S21	111	vliw.ti.	US-PGPUB; USPAT	OR	ON	2006/05/02 14:20
S22	1	S21 and latency.ab.	US-PGPUB; USPAT	OR	ON	2006/05/02 13:44
S23	3316	latency with instruction	US-PGPUB; USPAT	OR	ON	2006/05/02 13:44
S24	254	S23 with (variable or shorter or longer)	US-PGPUB; USPAT	OR	ON	2006/05/02 13:44
S25	53	S24 and vliw	US-PGPUB; USPAT	OR	ON	2006/05/02 13:44
S26	26	S21 and (latency near2 (instruction or execution))	US-PGPUB; USPAT	OR	ON	2006/05/02 14:15
S27	821	variable near3 latency	US-PGPUB; USPAT	OR	ON	2006/05/02 14:15
S28	123	S27 with (instruction or execution or operation)	US-PGPUB; USPAT	OR	ON	2006/05/02 14:15
S29	25	S28 and VLIW	US-PGPUB; USPAT	OR	ON	2006/05/02 14:15
S30	11	("3611306" "3781814" "4833599" "4920538" "4943912" "5050068" "5127092" "5179680" "5203002" "5313551" "5355460").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/02 14:24

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S31	11	("5450556" "5485629" "5590368" "5625835" "5649135" "5787303" "5826054" "5978822" "5983336" "6026478" "6026486").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/02 16:02
S33	25	(HDL with synthesis).ab.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 10:50
S34	69	"slow clock".ab.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 10:57
S35	1577	power with "clock speed"	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 10:58
S36	48	S35.ab.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 10:58